SV COLLEGE OF ENGINEERING
(AUTONOMOUS)
Karakambadi Road, Tirupati-517507

R 20

EXAMINATION BRANCH

M.Tech I Semester I Internal Examinations January-2023
TIME TABLE

AN: **02.30 pm to 04.30 pm**

Date/Day	VLSI Design	Computer Science and Engineering (CSE)
17-01-2023 (Tuesday)	CMOS Analog IC Design	Mathematical Foundations of Computer Science
18-01-2023 (Wednesday)	CMOS Digital IC Design	Advanced Data Structures
19-01-2023 (Thursday)	Nanomaterials & Nanotechnology	Machine Learning
20-01-2023 (Friday)	CPLD and FPGA Architectures and Applications	Data Science
21-01-2023 (Saturday)	Research Methodology and IPR	Research Methodology and IPR

CONTROLLER OF EXAMINATIONS   **PRINCIPAL**

Copy To:
1. ACE I/II/III.
2. HOD (ECE & CSE) with a request to circulate among Staff and students and make arrangements to send all the question papers (soft copy & hard copy) to the Exam cell on or before **11-01-2023,04.00 P.M.**
3. All Notice Boards, T & P Cell, I/C Security, I/C Hostel, I/C Transport, I/C Canteen and AO for information.

Date: 06/01/2023